



PATENT
Attorney Docket No. 10354*3

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)	
)	
John R. HUMPHREY et al.)	
)	Group Art Unit: 2186
Serial No.: 10/808,895)	
)	Examiner: Unassigned
Filed: March 24, 2004)	
)	
For: ORGANIZATION OF CACHE)	
MEMORY FOR HARDWARE)	
ACCELERATION OF THE)	
FINITE-DIFFERENCE TIME-)	
DOMAIN METHOD)	

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(b)

Pursuant to 37 C.F.R. §§ 1.56 and 1.97(b), Applicants bring to the attention of the Examiner the documents listed on the attached PTO-1449. This Information Disclosure Statement is being filed before the mailing date of a first Office Action on the merits for the above-referenced application. Copies of the listed documents are attached. Applicants respectfully request that the Examiner consider the listed documents and indicate that they were considered by making appropriate notations on the attached PTO-1449.

This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that each or all of the listed documents are material or constitute "prior art." If the Examiner applies any of the documents as prior art against any

claim in the application and Applicants determine that the cited documents do not constitute "prior art" under United States law, Applicants reserve the right to present to the office the relevant facts and law regarding the appropriate status of such documents. Applicants further reserve the right to take appropriate action to establish the patentability of the disclosed invention over the listed documents, should one or more of the documents be applied against the claims of the present application.

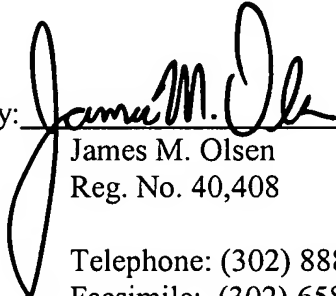
If there is any fee due in connection with the filing of this Statement, please charge the fee to our Deposit Account No. 03-2775.

Respectfully submitted,

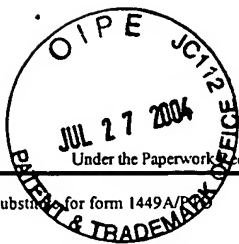
CONNOLLY BOVE LODGE & HUTZ LLP

Date: July 22, 2004

By: _____


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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Complete if Known	
				Application Number	10/808,895
				Filing Date	March 24, 2003
				First Named Inventor	John R. HUMPHREY et al.
				Group Art Unit	2186
				Examiner Name	Unassigned
Sheet	1	of	1	Attorney Docket Number	10354*3

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)				

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant

¹ Applicant's unique citation designation number (optional). ² See attached Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the application number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, page(s), volume-issue number(s), publisher, city and/or country where published.			
		J.R. Marek, "An Investigation of a Design for a Finite-Difference Time Domain (FDTD) Hardware Accelerator," Air Force Institute of Technology, Wright-Patterson AFB, M.S. Thesis (1991).			
		J.R. Marek et al., "A Dedicated VLSI Architecture for Finite-Difference Time Domain Calculations," presented at the 8th Annual Review of Progress in Applied Computational Electromagnetics, Naval Postgraduate School, Monterey, California pp. 546-553 (1992).			
		R.N. Schneider et al., "Application of FPGA Technology to Accelerate the Finite-Difference Time-Domain (FDTD) Method," presented at the 10th ACM International Symposium on Field-Programmable Gate Arrays, Monterey, California, pp. 97-105 (2002).			
		P. Placidi et al., "A Custom VLSI Architecture for the Solution of FDTD Equations," <i>IEICE Trans. Electron.</i> , vol. E85-C, No. 3, pp. 572-577 (March 2002).			

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

Examiner Signature		Date Considered	
Form PTO 1449		Patent and Trademark Office – U.S. Department of Commerce	

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